



PATENT
81788.0020

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Shota Kitamura, et al.

Serial No: 09/392,865

Filed: September 9, 1999

For: NON-VOLATILE SEMICONDUCTOR
MEMORY DEVICE AND ITS
MANUFACTURING METHOD

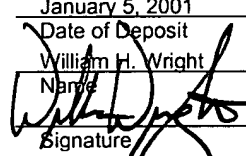
Art Unit: 2811

Examiner: T. Tran

#14/ Notice
of Appeal
R. Tyson
1/23/01

**NOTICE OF APPEAL FROM THE PRIMARY
EXAMINER TO THE BOARD OF APPEALS**

Box AF
Assistant Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: BOX AF, Assistant Commissioner for Patents, Washington D.C. 20231, on	
January 5, 2001	Date of Deposit
William H. Wright	Name
	Signature
1/5/01	Date

Dear Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated July 5, 2000 of the Primary Examiner finally rejecting claims 5-7, 16 and 17. The item(s) checked below are appropriate:

1. ☒ A Petition for Extension of Time to respond to the final rejection is enclosed with the proper fee.
2. ☐ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. The application is on behalf of
 - ☐ a small entity
 - Applicants claims small entity status.
 - ☒ other than a small entity.

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☒ Fee ☐ \$155 ☒ \$310

☒ enclosed.

☐ not required (fee paid in prior appeal).

☐ charged to Deposit Account No. 50-1314 if any additional fees are required.

(An additional copy of this Notice is enclosed.)

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: January 5, 2001

By: 

William H. Wright

Registration No. 36,312

Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900

Los Angeles, California 90071

Phone: 213-337-6700

Fax: 213-337-6701